

Fig. 1

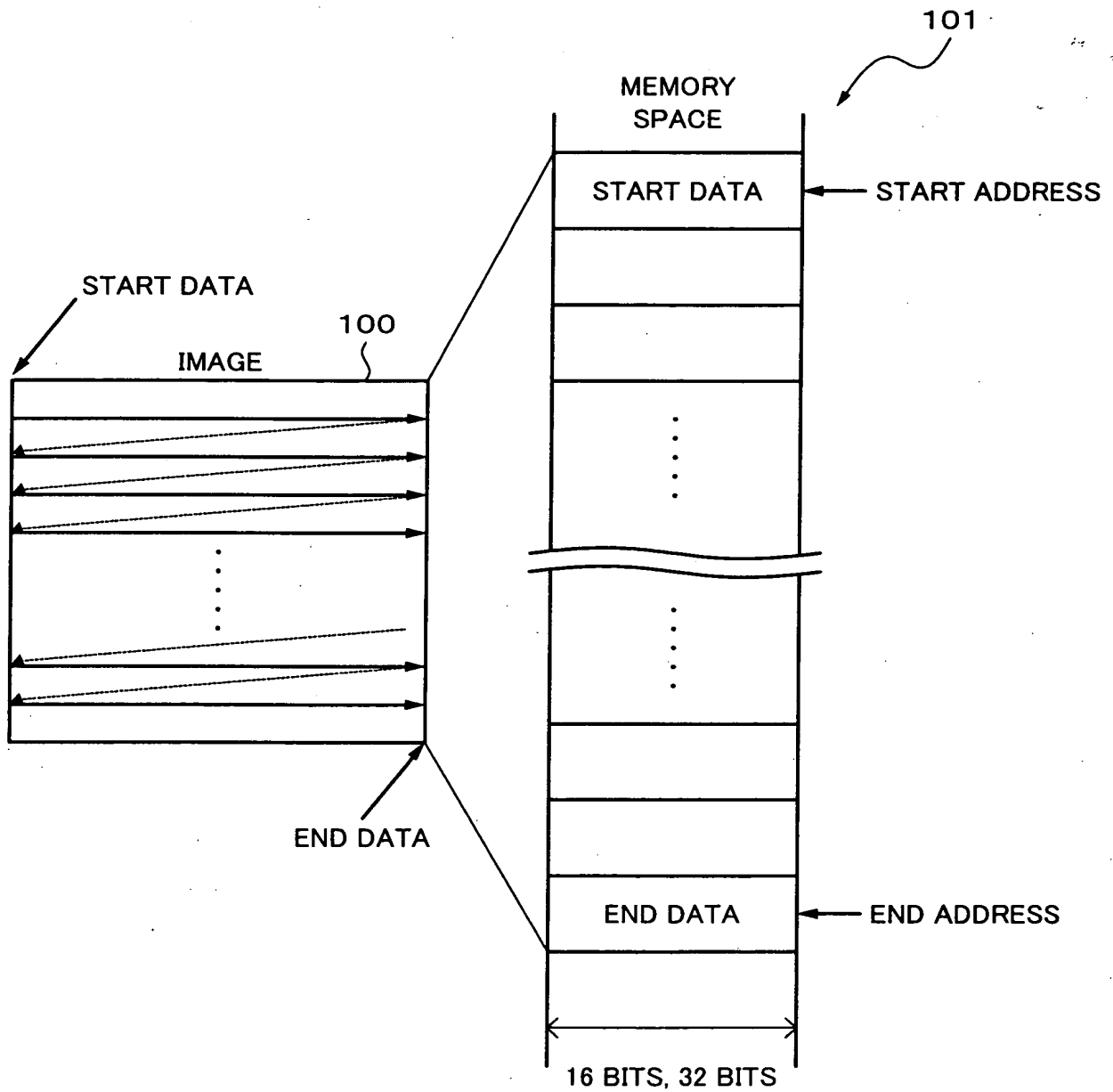


Fig. 2

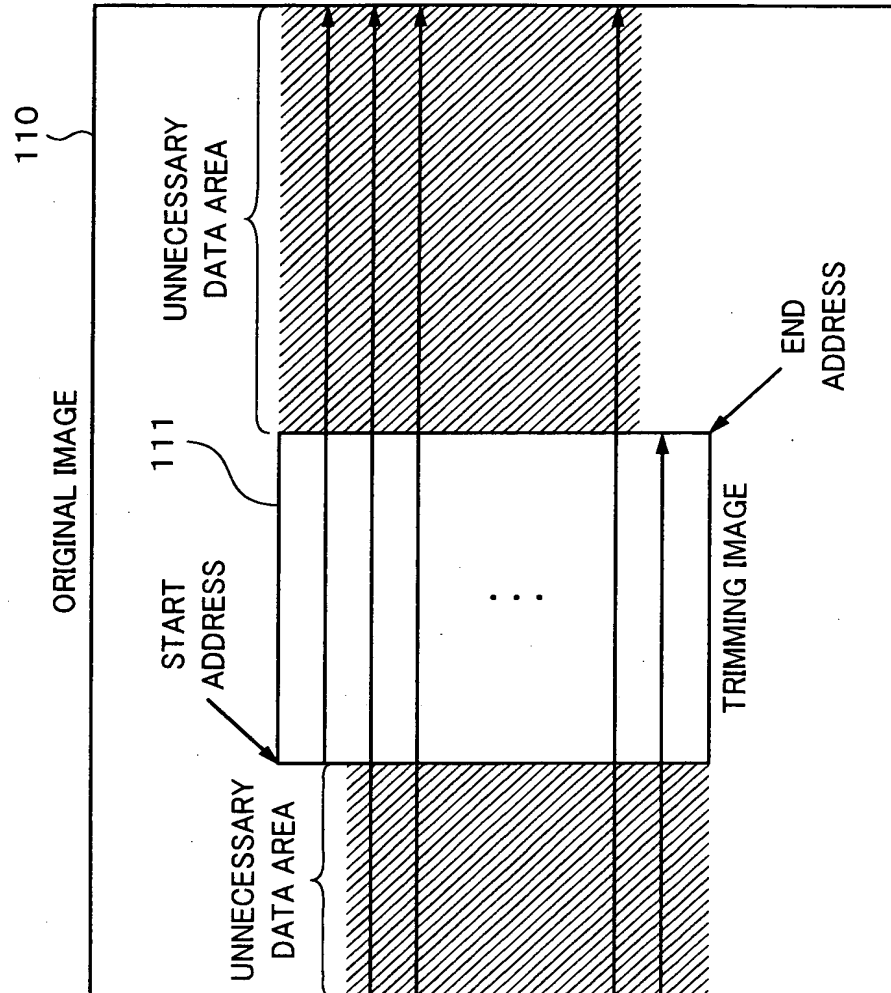


Fig. 3

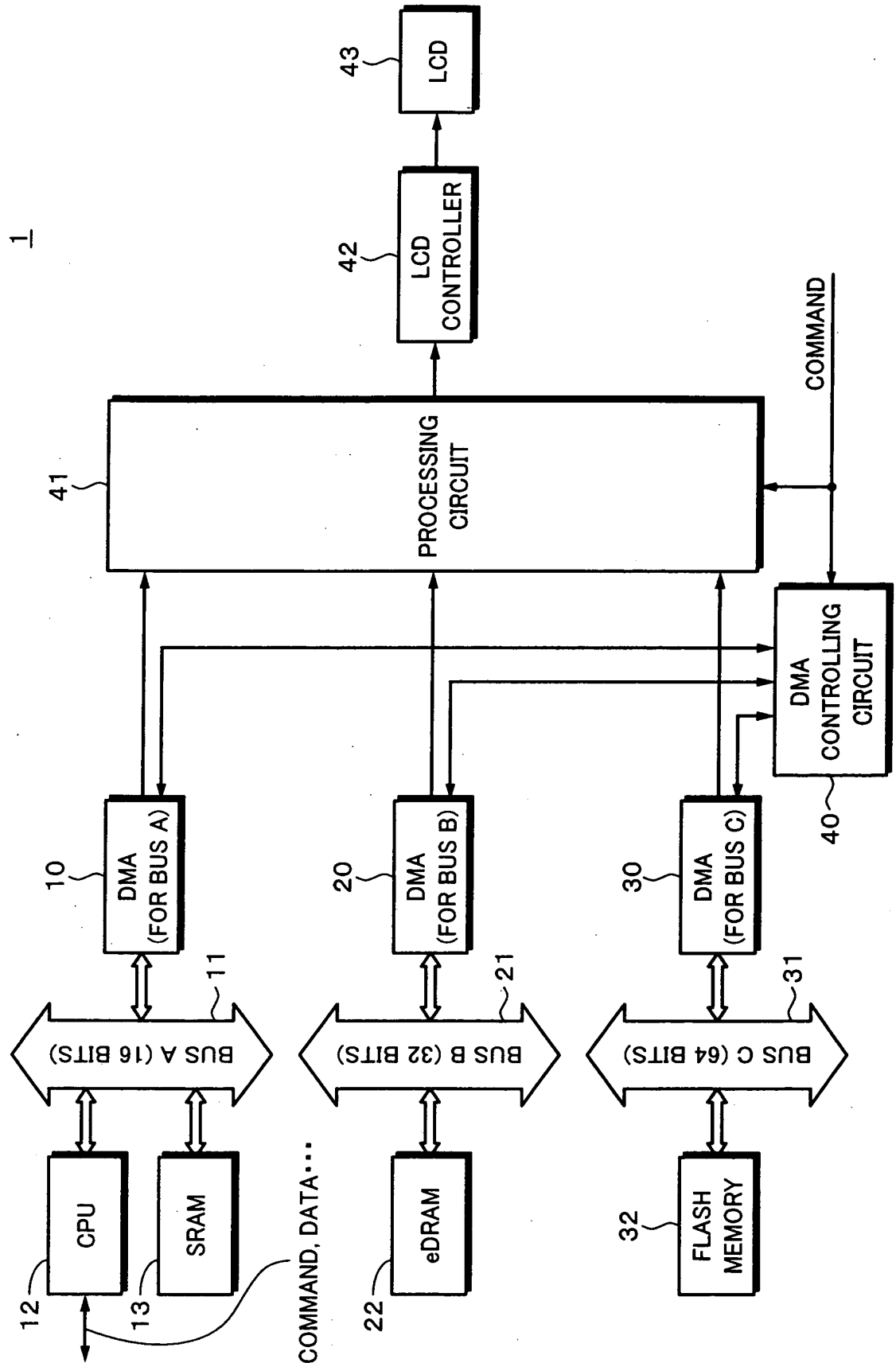


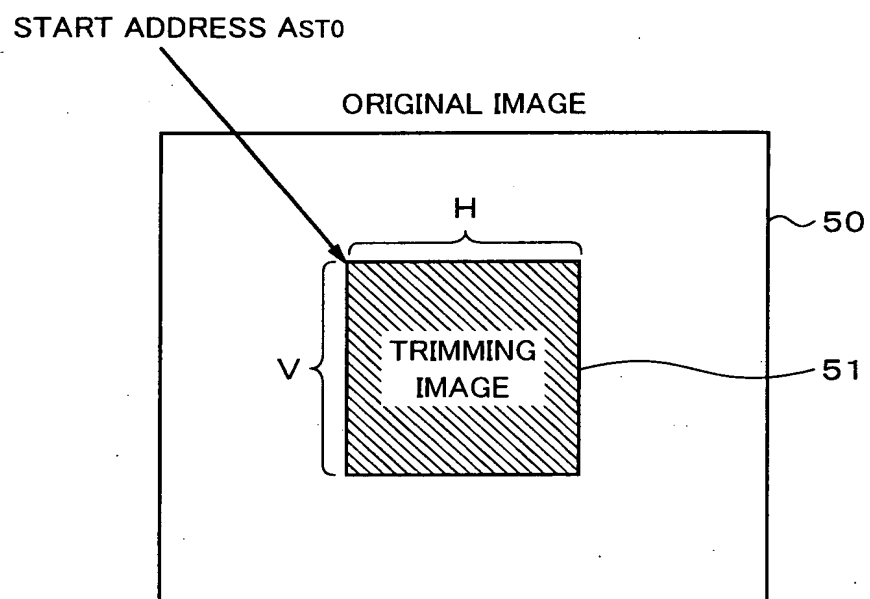
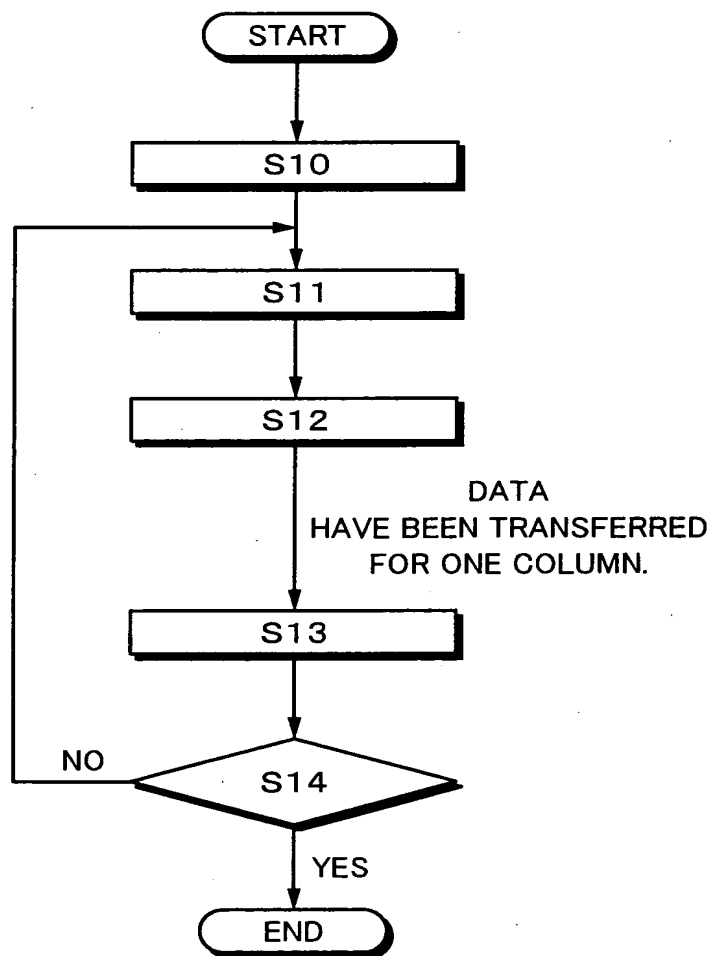
Fig. 4

Fig. 5

DESCRIPTION OF REFERENCE NUMERALS

- 1 IMAGE PROCESSING APPARATUS
- 10, 20, 30 DMA DEVICE
- 11, 21, 31 BUS
- 12 CPU
- 13 RAM
- 22 eDRAM
- 32 FLASH MEMORY
- 40 DMA CONTROLLING CIRCUIT
- 41 PROCESSING CIRCUIT
- S10 SET DATA OF START ADDRESS A_{ST0} , SIZES H AND V, AND SO FORTH OF TRIMMING IMAGE TO DMA CONTROLLING CIRCUIT.
- S11 DMA CONTROLLING CIRCUIT SENDS START ADDRESS A_{STn} OF NEXT COLUMN, TRANSFER SIZE H, AND START SIGNAL TO DMA DEVICE.
- S12 DMA DEVICE TRANSFERS DATA FOR ONE COLUMN.
- S13 DMA DEVICE SENDS END SIGNAL TO DMA CONTROLLING CIRCUIT.
- S14 HAVE DATA OF SIZE V BEEN TRANSFERRED ?